The schedule will be incrementally extended during the seminar. Please contact the organizers if you want to give a presentation.

**Monday, November 18**

09:00 – 09:45  *Desynchronizing Synchronous Systems*
Yu Bai (TU Kaiserslautern, Germany)

09:45 – 10:30  *libDGALS: The Library Approach to Dynamic GALS Systems and its Application in Fault-Tolerant Systems*
Wei-Tsun Sun (INRIA Grenoble, France)

Coffee Break

10:45 – 11:30  *Representing Spatially Moving Entities using Time-Variant Topologies*
Fernando Barros (University of Coimbra, Portugal)

11:30 – 12:15  *Functioning Hardware from Functional Specifications*
Stephen Edwards (Columbia University, New York, USA)

Lunch Break

14:00 – 14:45  *Index theory for Hybrid DAE Systems*
Benoit Caillaud (INRIA Rennes, France)

14:45 – 15:30  *Modal Interface Automata*
Gerald Lüttgen (University Bamberg, Germany)

Coffee Break

16:00 – 16:45  *The Coroutine Model of Computation*
Chris Shaver (University of California at Berkeley, USA)

16:45 – 17:30  *A prototype compiler for a bursty synchronous language*
Adrien Guatto (ENS, Paris, France)

17:30 – 18:00  *Wrap up Discussion*
Tuesday, November 19

09:00 – 09:45  
*A Tale of Two Semantics: Clocked Dimensions in a Multidimensional Language*
John Plaice (University of New South Wales, Australia)

09:45 – 10:30  
*TBD*
Michael Mendler (University of Bamberg, Germany)

Coffee Break

10:45 – 11:30  
*SCCharts*
Christian Motika (University of Kiel, Germany)

11:30 – 12:15  
*Synthesis of Sequentially Constructive Programs*
Reinhard von Hanxleden (University of Kiel, Germany)

Lunch Break

14:00 – 14:45  
*Interactive Verification of Cyber-physical Systems*
Manuel Gesell and Xian Li (TU Kaiserslautern, Germany)

14:45 – 15:30  
*A Causality Analysis for Hybrid Modelers*
Marc Pouzet (ENS, Paris)

Coffee Break

16:00 – 16:45  
*WCET Enhancement for Synchronous Programs*
Pascal Raymond (Verimag, Grenoble, France)

16:45 – 17:30  
*Towards a Formal Software Design Methodology for Predictable Embedded Multiprocessor Applications*
Ingo Sander (KTH Royal Institute of Technology, Kista, Sweden)

17:30 – 17:45  
*A Fly from Girona to Barcelona*
Marc Pouzet (ENS, Paris)

17:45 – 18:00  
*Wrap up Discussion*
Wednesday, November 20

09:00 – 09:45  *From Synchronous to Timed Programming*
Eleftherios Matsikoudis (University of California at Berkeley, USA)

09:45 – 10:30  *Behavioral Equivalence of Transducers under a Fixed Protocol*
Dan Ghica (University of Birmingham, UK)

Coffee Break

10:45 – 11:30  *Modelyze: Embedding Equation-Based DSLs*
David Broman (University of California at Berkeley, USA)

11:30 – 12:15  *Coordination Programming in AstraKhan*
Alex Shafarenko (University of Hertfordshire, UK)

Lunch Break

13:15 – 13:30  *group photo at the chapel*

13:30 – 16:30  hiking (ask Reinhard)
Thursday, November 21

09:00 – 09:45  *Causality and Compositionality: Comparing the different views*  
Gérard Berry (INRIA Sophia Antipolis – Méditerranée, France)

09:45 – 10:30  *Modular control for the coordination of autonomic managers*  
Gwenaël Delaval (University of Grenoble – LIG, France)

Coffee Break

10:45 – 11:30  *Physical Semantics of the Reactive Model*  
Louis Mandel (College de France, Paris, France)

11:30 – 12:15  *From quasi-synchrony to LTTL*  
Guillaume Baudart (ENS Paris, France)

Lunch Break

14:00 – 14:45  *Precise Timing Analysis of Direct-Mapped Caches*  
Alain Girault (INRIA Grenoble – Rhône-Alpes, France)

14:45 – 15:30  *Debugging and Compiler Bootstrapping with an Equation-Based Language Compiler*  
Peter Fritzson (Linköping University, Sweden)

Coffee Break

16:00 – 16:45  *Safety Issues in CCSL specifications*  
Frédéric Mallet (INRIA Sophia Antipolis – Méditeranée, France)

16:45 – 17:30  *BPDF: A Statically Analyzable Dataflow Model with Integer and Boolean Parameters*  
Vagelis Bebelis (Inria Grenoble, France)

17:30 – 18:00  *Wrap up Discussion*
Friday, November 22

09:00 – 09:45  Integrated WCET Estimation of Multicore Applications  
Dumitru Potop-Butucaru (INRIA Rocquencourt, France)

09:45 – 10:30  Towards Discrete Controller Synthesis for the Reactive Adaptation of Autonomic Systems  
Nicolas Berthier (INRIA Rennes, France)

Coffee Break

10:45 – 11:30  Improving Fixpoint Approximation in Program Analysis  
Nicolas Halbwachs (VERIMAG, Grenoble, France)

11:30 – 12:15  A type system for synchronous pipelining  
Dan Ghica (University of Birmingham, UK)

Lunch